IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Tsukasa YONEYAMA et al.

Conf.

Application No. NEW NATIONAL PHASE

Group

Filed November 15, 2005

Examiner

NRD GUIDE CONVERTER AND CONNECTED STRUCTURE OF DIELECTRIC AND CONDUCTOR

INFORMATION DISCLOSURE STATEMENT (SUBMISSION CONCURRENT WITH THE FILING OF A NEW PATENT APPLICATION)

Assistant Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

November 15, 2005

Sir:

Pursuant to 37 C.F.R. §§ 1.97 and 1.98, and in fulfillment of the duty of disclosure under 37 C.F.R. § 1.56, applicant(s) hereby submit(s) an Information Disclosure Statement for consideration by the Examiner.

I. LIST OF PATENTS, PUBLICATIONS OR OTHER INFORMATION

The patents, publications, or other information submitted for consideration by the Office are listed on PTO-1449, attached hereto.

II. COPIES

- Copies of the U.S. patents or publications are not submitted since the USPTO has waived their submission for applications filed after June 30, 2003.
- Submitted herewith is a legible copy of (i) each foreign patent; (ii) each publication or that portion which caused it to be listed; and (iii) all other information or that portion which caused it to be listed.
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10/556911 JC12 Rec'd Docket No. 8075-1019

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This concurrentl therefore,	Informaly with	ation Disclosure Statement is being filed the filing of a new patent application; is required.							
If The Examiner has any questions concerning this IDS, he/she is requested to contact the undersigned.									
		Respectfully submitted,							
		YOUNG & THOMPSON							
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^{*} English language abstract provided for the Examiner's convenience